Notice of Allowability	10/685,371	
Notice of Allowability		CRAIN ET AL.
notice of Anomability	Examin r	Art Unit
	VanThu Nguyen	2824
The MAILING DATE of this communication ap All claims being allowable, PROSECUTION ON THE MERITS I herewith (or previously mailed), a Notice of Allowance (PTOL-8 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3	IS (OR REMAINS) CLOSED in this 5) or other appropriate communica RIGHTS. This application is subjection.	s application. If not included
1. This communication is responsive to		
2. The allowed claim(s) is/are <u>1-16</u> .		
3. The drawings filed on 14 October 2003 are accepted by		
4. Acknowledgment is made of a claim for foreign priority	under 35 U.S.C. § 119(a)-(d) or (f)	
a) ☐ All b) ☐ Some* c) ☐ None of the:		
 Certified copies of the priority documents have 		
Certified copies of the priority documents have	ve been received in Application No	D
Copies of the certified copies of the priority d	locuments have been received in t	his national stage application from the
International Bureau (PCT Rule 17.2(a))		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be subtracted the subtraction of the subtract	MENT of this application. mitted. Note the attached EXAMIN	EDIS AMENDMENT - NOTICE OF
IN CHWAL FATENT APPLICATION (PTO-152) Which give	ves reason(s) why the oath or decl	aration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") mu	ist be submitted.	
(a) ☐ including changes required by the Notice of Draftsper	rson's Patent Drawing Review(P1	ΓO-948) attached
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in	1.84(c)) should be written on the dra the header according to 37 CFR 1.12	wings in the front (not the back) of 21(d).
 DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT 	osit of BIOLOGICAL MATERIA FOR THE DEPOSIT OF BIOLOG	L must be submitted. Note the ICAL MATERIAL.
Attachm nt(s) 1 ☑ Notice of References Cited (PTO-892)	- -	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		Patent Application (PTO-152)
	6. ☐ Interview Summa Paper No./Mail D	ry (PTO-413), Date
B. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	08), 7. 🗌 Examiner's Amer	ndment/Comment
1. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's Stater	ment of Reasons for Allowance
, of Biological Material	9. 🗌 Other	while
		VanThu Nguyen Primary Examiner Art Unit: 2824

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Allowable Subject Matter

1. Claims 1-16 are allowed.

The following is a statement of reasons for the indication of allowance:

The prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. Black et al., Chrissostomidis et al. and Calder et al., taken individually or in combination, do not teach the claimed invention being used as sense amplifier for reading the contents of a memory cell in a transponder in RFID system, in combination with the remaining claimed limitations.

Chrissostomidis et al. disclose, in FIG. 2, a circuit comprises a first transistor T1 for receiving a current Iref, a second transistor arranged for mirror the read current T2, a capacitor arranged to integrate the mirrored current and provide a first voltage Vcomp, means T5 for resetting the capacitor after a time period, a second voltage Vref, a comparator for comparing Vref and Vcomp. However, Chrissostomidis et al. are totally silent about the circuit can be use as sense amplifier for reading the contents of a memory cell.

Calder et al. disclose, in FIG. 2, a circuit comprises first transistor, second transistor, capacitor CINT, comparator. Calder et al. are also silent about the circuit can be use as sense amplifier for reading the contents of a memory cell.

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VanThu Nguyen whose telephone number is (571) 272-1881. The examiner can normally be reached on Monday-Friday, 8:00am-4:30pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

VTN May 19, 2004 VanThu Nguyen Primary Examiner Art Unit 2824